Combinative and sequential elements

General notes

1 All qualifying symbols inside the outline are defined in terms of the internal logic states of the relevant inputs and outputs (see sections 1, 2, and 3 of A00269).

2 In many cases, examples are based on commercial devices, and terminal numbers (for one unspecified package type) have been shown for the assistance of the reader. Where the type number implies the product of a specific manufacturer, this is done to avoid uncertainties caused by functional variations that sometimes occur between devices that have the same generic portion of the type number and are made by different manufacturers.

3 Where the logic polarity indicator has not been used, positive logic convention is assumed.

4 A given element may be symbolized in more than one way depending on the purpose it serves in the system (for example, symbols S01588 (12-28-10) and S01589 (12-28-11)). Also, use is often made of the complementary representation especially of combinative elements to enhance the understanding of the diagram. For example, an OR element is shown by the symbol for an AND but with negated inputs and outputs. In any case, the choice of the symbol should be governed by the relevant application of the element being shown on the diagram (see IEC 61082-1 for detailed information).