

The following symbols shall be oriented as described or shown within this standard with respect to the inputs, outputs and outlines of the elements in which they appear. That is, these symbols, together with any associated terminal lines, shall be mirrored when the direction of signal flow is reversed:

S01239 (10-15-01) Amplifier, general symbol  
S01466 (12-07-01) Logic negation, shown at an input  
S01467 (12-07-02) Logic negation, output  
S01468 (12-07-03) Polarity indicator, input  
S01469 (12-07-04) Polarity indicator, output  
S01470 (12-07-05) Polarity indicator, input, right to the left  
S01471 (12-07-06) Polarity indicator, output, right to the left  
S01472 (12-07-07) Dynamic input  
S01473 (12-07-08) Dynamic input with logic negation  
S01474 (12-07-09) Dynamic input with polarity indicator  
S01475 (12-08-01) Internal connection  
S01477 (12-08-03) Internal connection with dynamic character  
S01478 (12-08-04) Internal connection with negation and dynamic character  
S01479 (12-08-05) Internal input (left hand side)  
S01480 (12-08-05A) Internal input (right-hand side)  
S01481 (12-08-06) Internal output (virtual output)  
S01499 (12-09-08A) Output with special amplification  
S01498 (12-09-08B) Input with special amplification  
S01516 (12-09-24) Bit grouping for multibit input  
S01517 (12-09-25) Bit grouping for multibit output  
S01540 (12-09-47) Line grouping at the input side  
S01541 (12-09-48) Line grouping at the output side