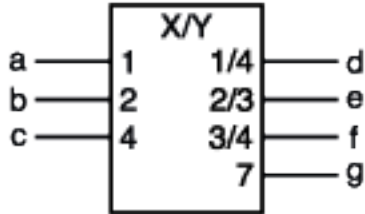## Illustration

Output d stands at its internal 1-state for the following combinations of internal logic states at inputs a, b and c:

| a=1 | b=0 | c=0 |
| a=0 | b=0 | c=1 |